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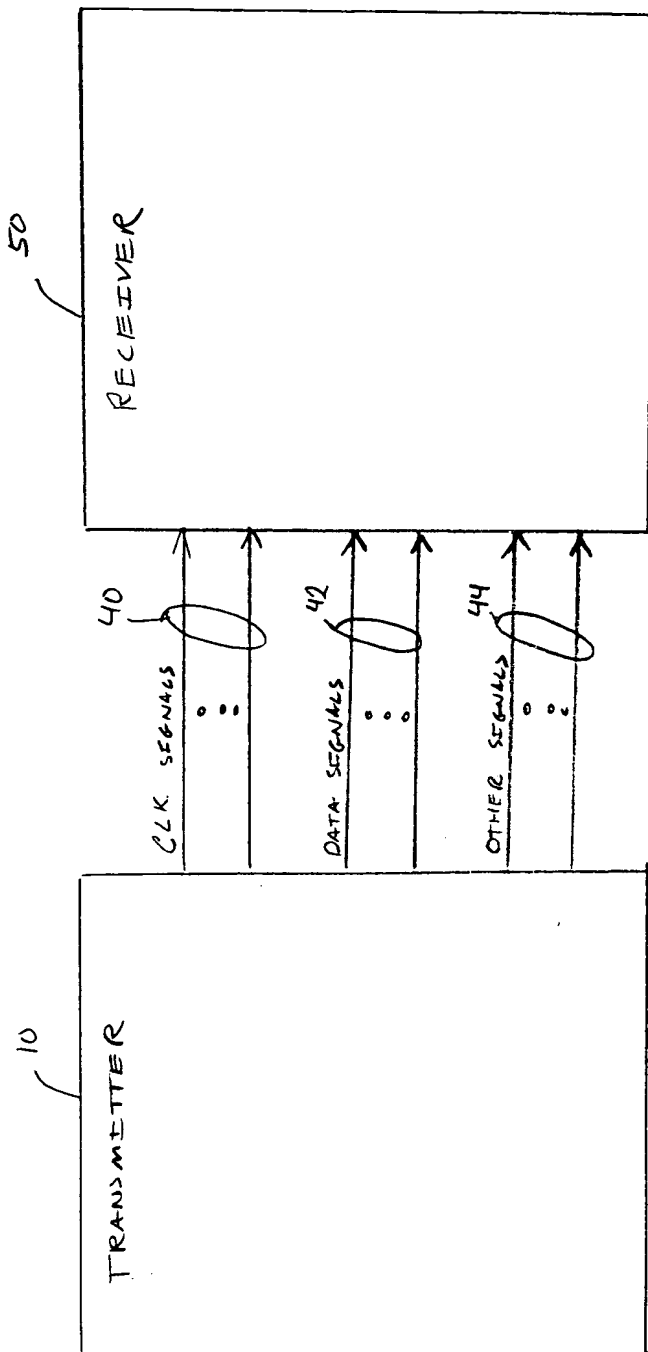


FIG. 1

TRANSMITTER

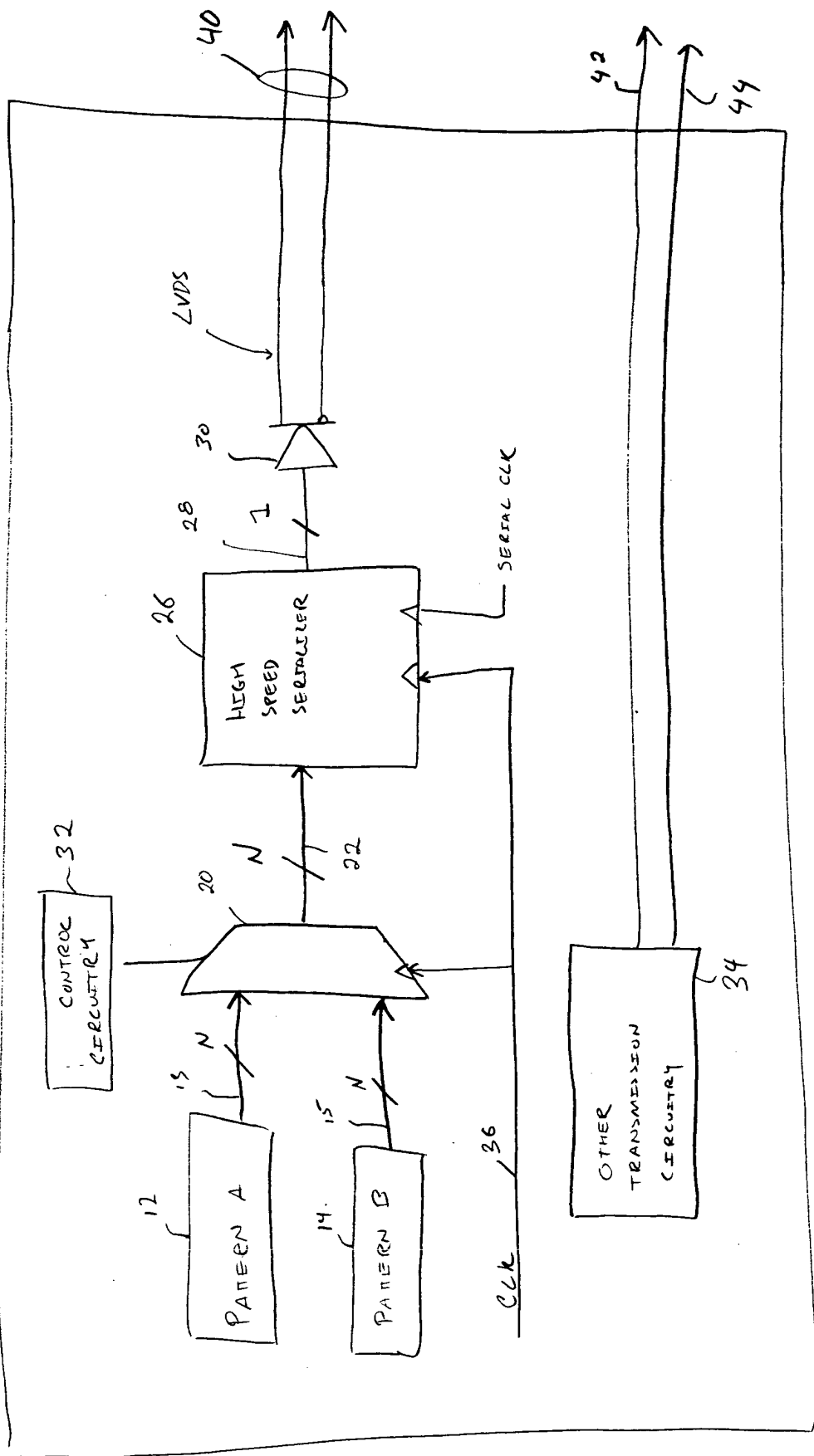


FIG. 2

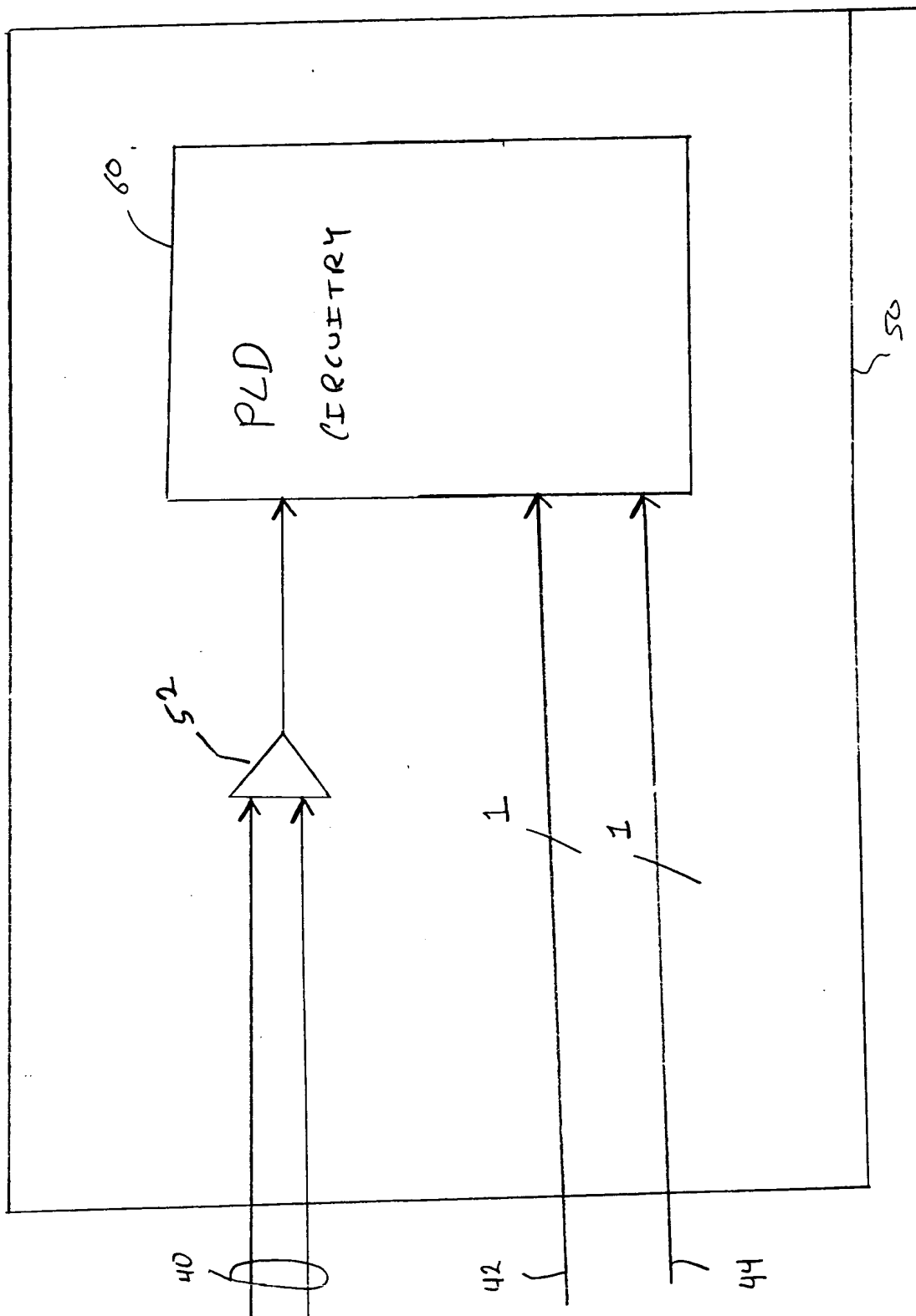
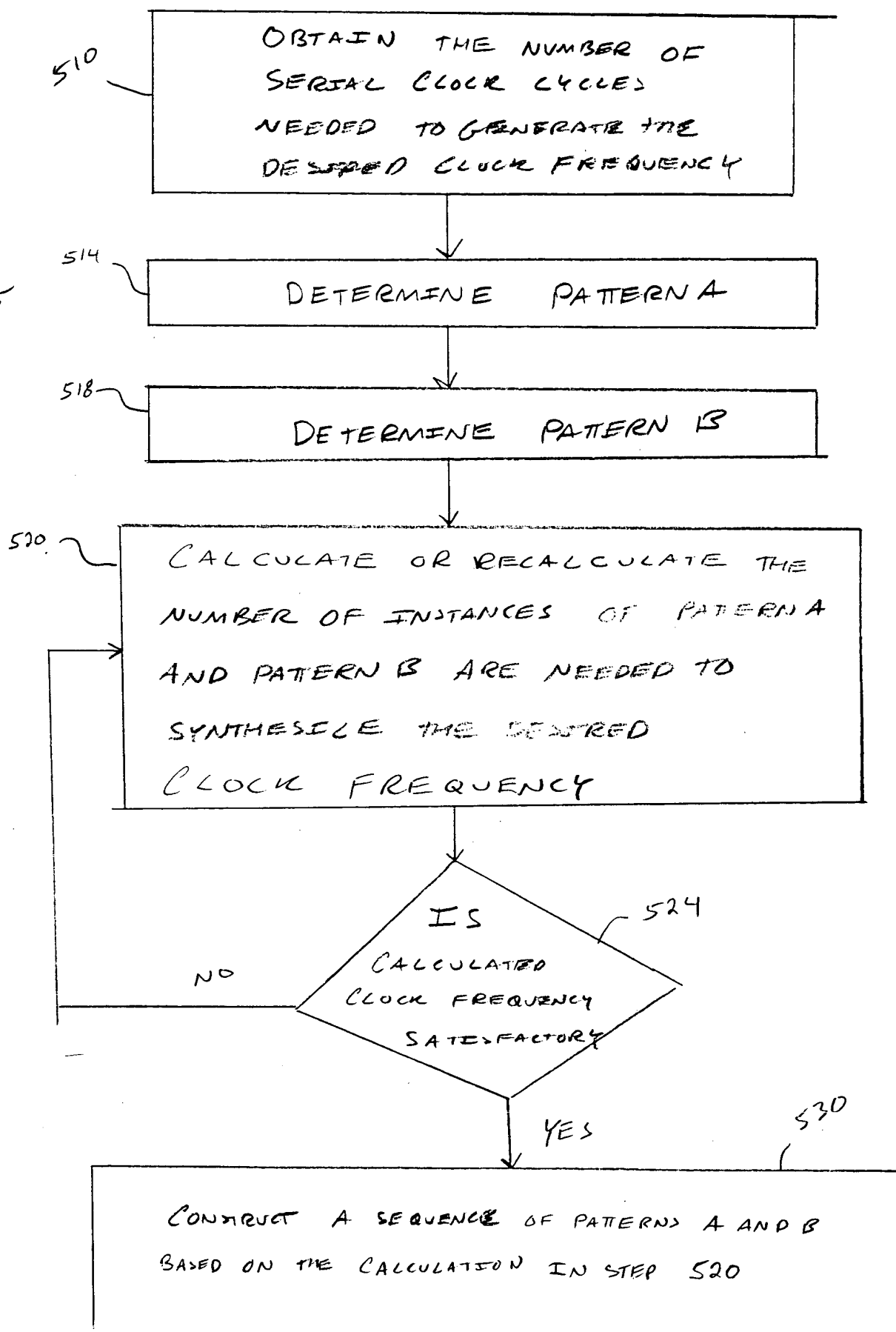


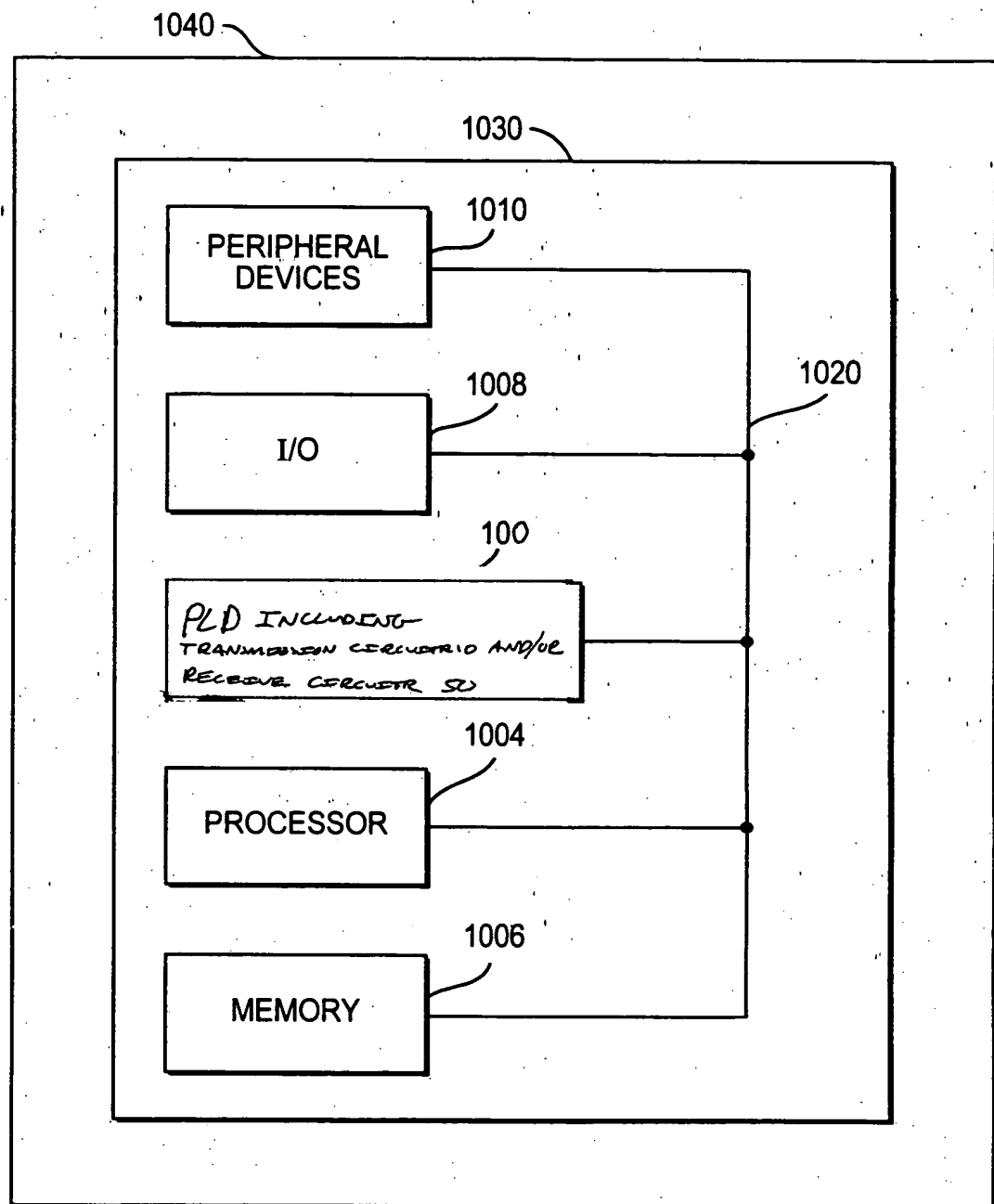
FIG. 3

Row	Serial Clock (Hz)	Desired Synthesized Clock (Hz)	Serial Clocks Per Synthesized Clock	Pattern A			Pattern B			Instances of Pattern A	Instances of Pattern B	Average Clock Rate Over Time (Hz)	Difference in Frequency (Hz)
				High	Low	Equivalent Frequency (Hz)	High	Low	Equivalent Frequency (Hz)				
1	8.40E+08	2.62500E+07	32	16	0	5.25E+07	0	16	5.25E+07	1	1	2.6250000E+07	0
2	8.40E+08	5.4000E+07	15.55556	7	8	5.60E+07	8	8	5.25E+07	3	4	5.4000000E+07	0.00
3	8.40E+08	5.3930E+07	15.57575	7	8	5.60E+07	8	8	5.25E+07	20000	28951	5.3930001E+07	-1.43
4	8.40E+08	5.3930E+07	15.57575	7	8	5.60E+07	8	8	5.25E+07	48095	69620	5.3930000E+07	-0.42
5	8.40E+08	4.0456E+07	20.76354	10	10	4.20E+07	10	11	4.00E+07	10002	33912	4.0455527E+07	1.29
6	8.40E+08	3.9450E+07	21.29278	10	11	4.00E+07	11	11	3.82E+07	68000	29491	3.9450001E+07	-0.51
7	8.40E+08	5.3900E+07	15.58442	7	8	5.60E+07	8	8	5.25E+07	2	3	5.3900000E+07	0.00

FIG. 4

FIG. 5





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FIG. 6